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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of :

Hiroshi YAMAUCHI et al. :

Docket No. 2001_0929A

Serial No. 09/892,746 :

Group Art Unit 2833

Filed June 28, 2001 :

AGGREGATE OF ELECTRONIC COMPONENTS
AND MOUNTING METHOD THEREOF

THE COMMISSIONER IS AUTHORIZED
TO CHARGE ANY DEFICIENCY IN THE
FEE FOR THIS PAPER TO DEPOSIT
ACCOUNT NO. 23-0975.

CLAIM OF PRIORITY UNDER 35 USC 119

Assistant Commissioner for Patents,
Washington, DC 20231

Sir:

Applicants in the above-entitled application hereby claim the date of priority under the International Convention of Japanese Patent Application No. 2000-199456, filed June 30, 2000, as acknowledged in the Declaration of this application.

A certified copy of said Japanese Patent Application is submitted herewith.

Respectfully submitted,

Hiroshi YAMAUCHI et al.

By



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